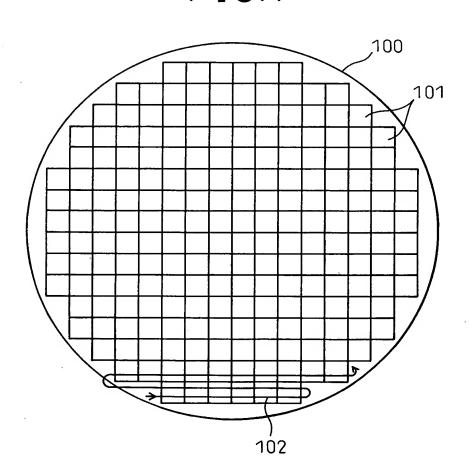
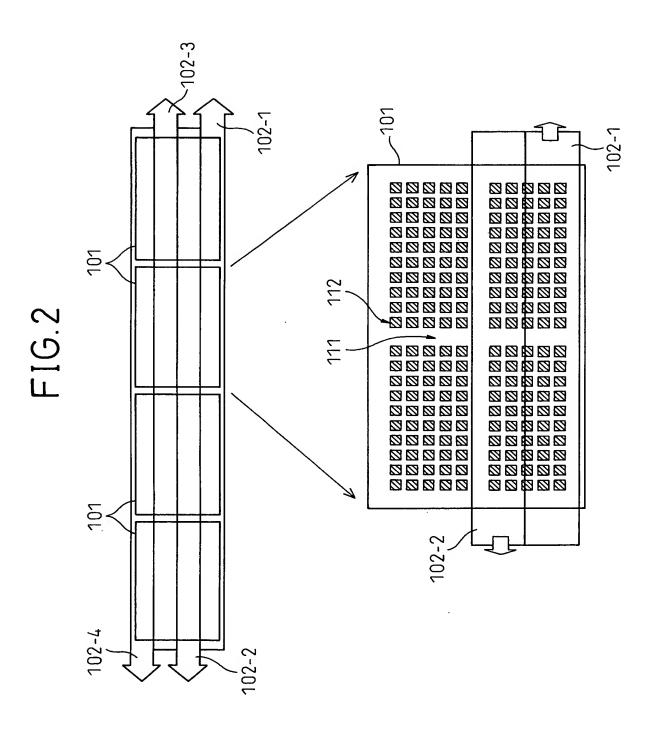
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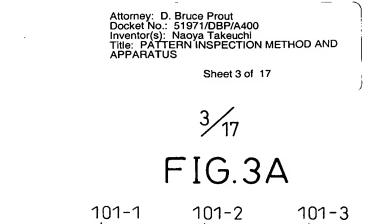
FIG.1



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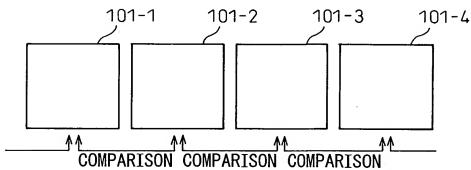


FIG.3B

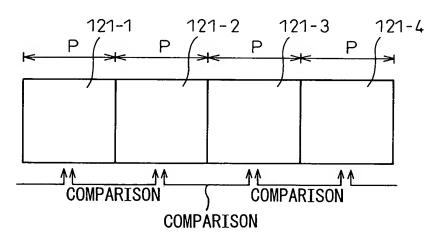
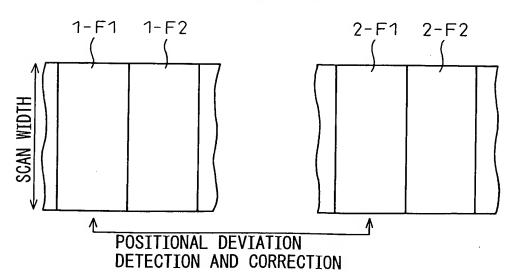
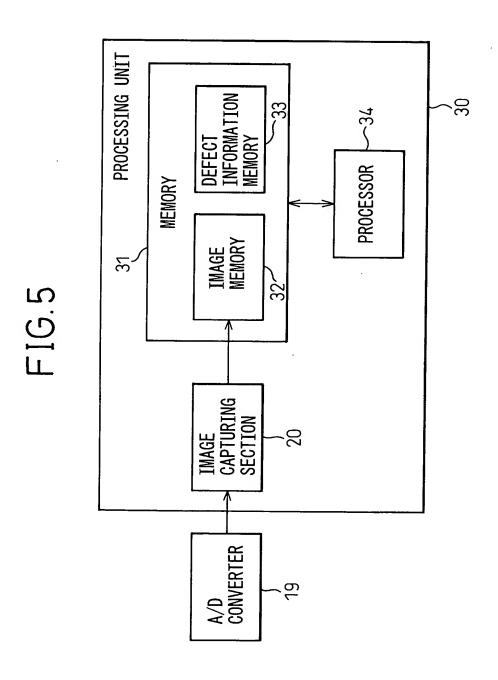


FIG.3C

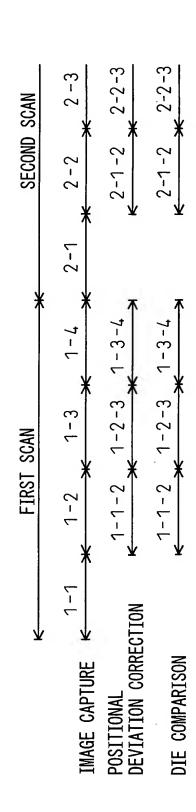


4/17 DEFECT INFORMATION DATABASE 92 INFORMAT STORAGE SECTION DEFECT JUDGING SECTION DIFFERENCE DETECTING SECTION PROCESSING SECTION 22 Яb FIG. 4 IMAGE DELAYING MEMORY IMAGE CAPTURING SECTION 20 CONVERTER 9 9 CAMERA

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DIE COMPARISON INSPECTION

FIG.6A

FIG.6B

CELL COMPARISON INSPECTION

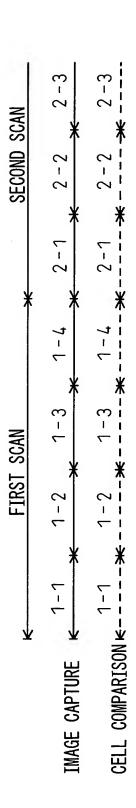
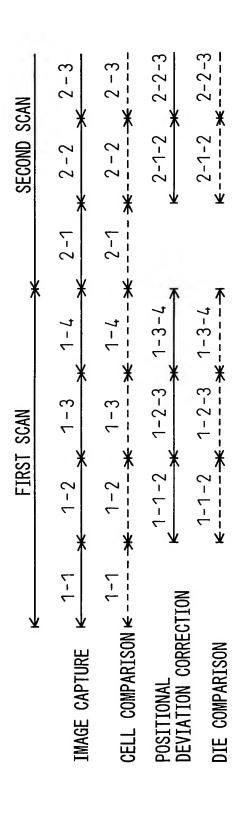
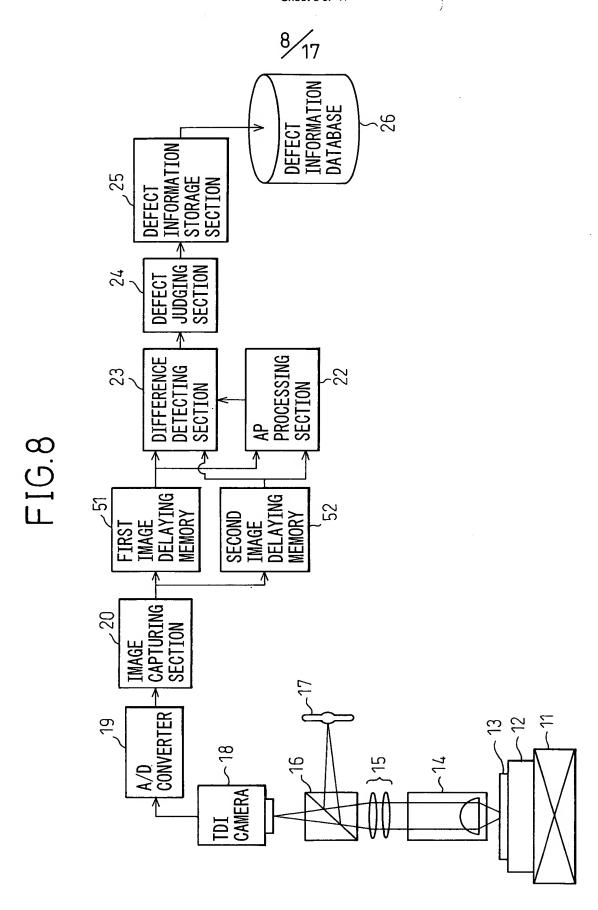


FIG.7

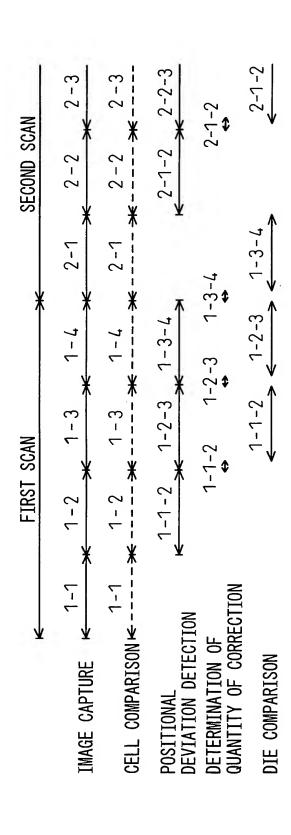
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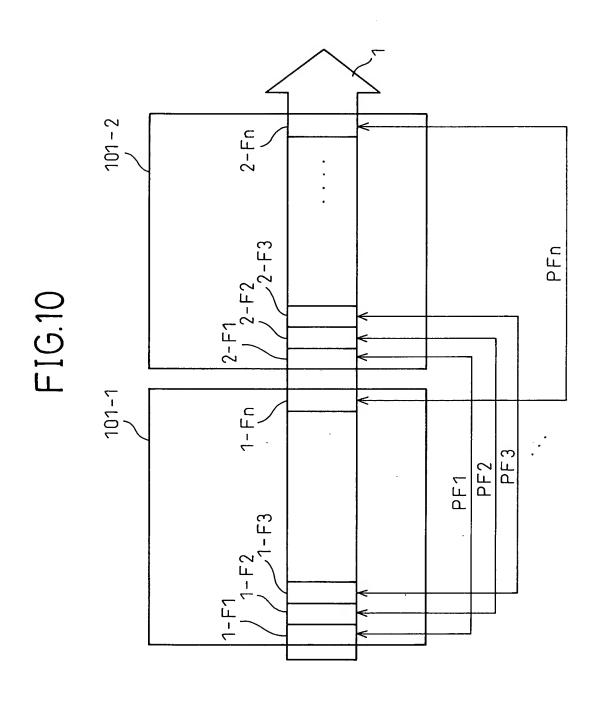
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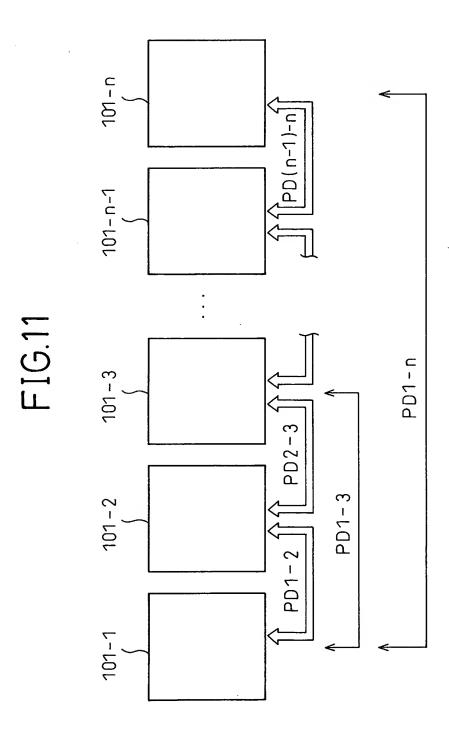
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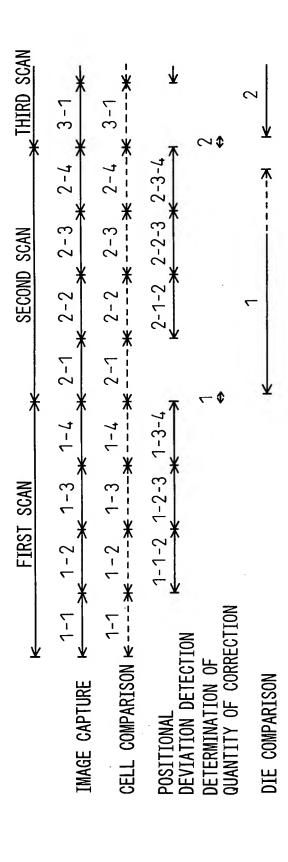
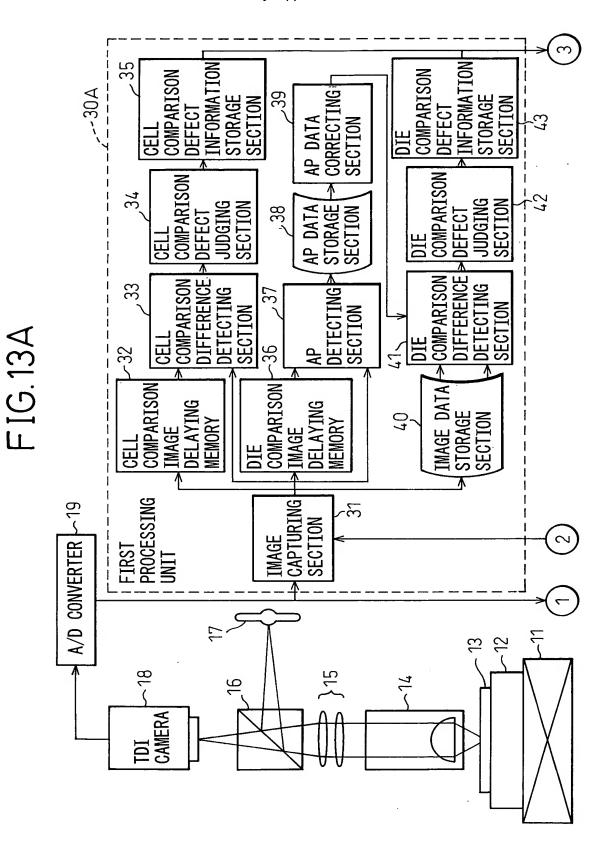


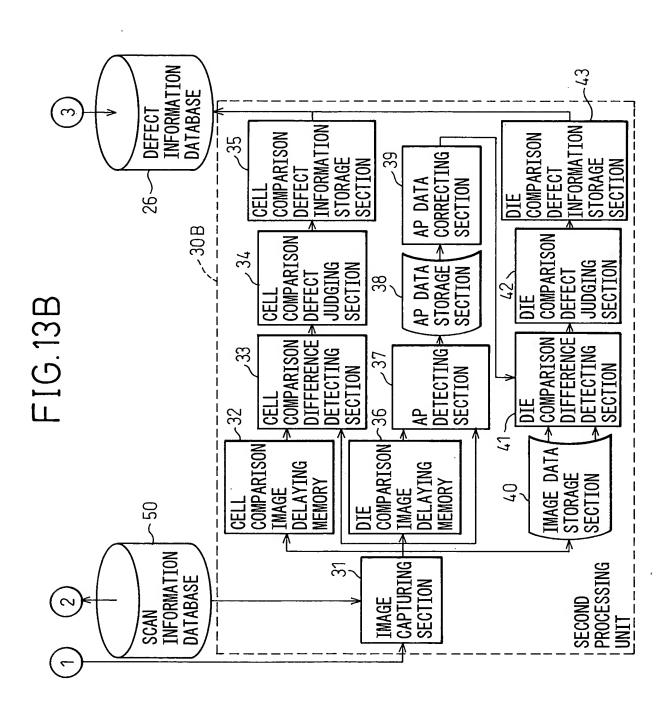
FIG.12

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Attorney: D. Bruce Prout Docket No.: 51971/DBP/A400 Inventor(s): Naoya Takeuchi Title: PATTERN INSPECTION METHOD AND APPARATUS Sheet 15 of 17 15/17 \$ FOURTH SCAN SECOND SCAN __ THIRD SCAN FIG.14 \$ \$ FIRST SCAN DETERMINATION OF QUANTITY OF CORRECTION DETERMINATION OF QUANTITY OF CORRECTION POSITIONAL DEVIATION DETECTION[™] POSITIONAL DEVIATION DETECTION CELL COMPARISON CELL COMPARISON SECOND UNIT IMAGE CAPTURE DIE COMPARISON DIE COMPARISON IMAGE CAPTURE FIRST UNIT

16/17

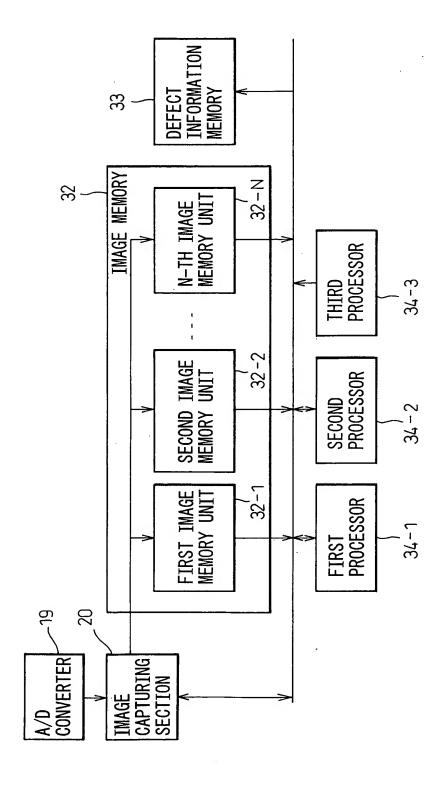


FIG.15

D6R

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DSR D2R FIRST PROCESSOR $\vdash - \subseteq 1$

FIG.16

DSF PSF 90 D4R C2 [C5 D3R D3F DIR.

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THIRD PROCESSOR

SECOND PROCESSOR

CELL COMPARISON AND POSITIONAL DEVIATION DETECTION IN N-TH ROW

DETERMINATION OF QUANTITY OF CORRECTION

Ħ

COMPARISON IN FIRST HALF IN N-TH ROW DIE PNF

DIE COMPARISON IN SECOND HALF IN N-TH ROW DNR I